

PATENT ABSTRACTS OF JAPAN

(11) Publication number : 03-030022

(43) Date of publication of application : 08.02.1991

(51) Int. Cl.

G06F 9/38

G06F 11/14

(21) Application number : 01-166082

(71)Applicant : KOUFU NIPPON DENKI KK

(22) Date of filing : 27.06.1989

(72) Inventor : ASANO SADAJI

(54) INFORMATION PROCESSOR

(57) Abstract:

PURPOSE: To execute the retry of an instruction by specifying an instruction to which in-buffer information in which an error is generated belongs and executing a warning, in the case an error is detected by a write control to a buffer between a prefetch part and an operation executing part.

CONSTITUTION: An error detecting signal 501 is outputted in the case a write instructing signal 201 is sent out of a prefetch part 2 in spite of a fact that a full signal 131 is sent to the prefetch part 2 from a circuit 13. Also, when an error display signal 601 is sent out once, a flag 52 is set, and the error detecting signal 501 displays continuously an error. After a malfunction of a signal 502, an error display which is read out first is nullified. When an error is detected by a malfunction detecting circuit 5, a flag 53 is set, and the flag 53 is reset, when a read instructing signal 301 is outputted from an operation executing part 3. In such a way, the retry of an instruction can be executed.

LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

